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Application for United States

PATENT
B10-25562US

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

MRAM ARCHITECTURES FOR INCREASED WRITE SELECTIVITY

The specification of which

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

(Number) **(Country)** **(Day/Month/Year Filed)** **Yes** **No**

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.) (Filing Date) (Status) (patented, pending, abandoned)

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: KRIS T. FREDRICK (Reg. No. 42,554) JOHN G. SHUDY, JR. (Reg. No. 31,214), IAN D. MACKINNON (Reg. No. 34,660). Address all telephone calls to KRIS T. FREDRICK at telephone number (612) 951-6029.

Address all correspondence to Kris T. Fredrick, Customer Number 000128

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Sole
or First Inventor Shaoping Li

Inventor's Signature Shi Li Date 11/19, 2000

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Inventor's Signature theodore zhu Date Oct. 13, 2000

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Inventor's Signature Anthony Arrott Date 11/13, 2000

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Full Name of Sole or First Inventor William L Larson

Inventor's Signature William L Larson Date 10/16, 2000

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Citizenship USA

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Full Name of Sole or First Inventor Yong Lu

Inventor's Signature / Date 10/13, 2000

Residence Plymouth, Hennepin County, Minnesota

Citizenship Canada

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Plymouth, MN 55442

*Title 37, Code of Federal Regulations §1.56:

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is cancelled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§ 1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

(1) prior art cited in search reports of a foreign patent office in a counterpart application, and

(2) the closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.

(b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and

(1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; r

(2) It refutes, or is inconsistent with, a position the applicant takes in:

(i) Opposing an argument of unpatentability relied on by the Office, or

(ii) Asserting an argument of patentability.

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A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

(c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:

- (1) Each inventor named in the application;
- (2) Each attorney or agent who prepares or prosecutes the application; and

(3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

(d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.

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MICRON.198A

MICRON.173GEN

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	MICRON TECHNOLOGY, INC.)
App. No.	:	listed below)
Filed	:	listed below)
For	:	listed below)
Examiner	:	Unknown)

ESTABLISHMENT OF RIGHT OF ASSIGNEE TO TAKE ACTION
AND
REVOCATION AND POWER OF ATTORNEY

United States Patent and Trademark Office
P.O. Box 2327
Arlington, VA 22202

Dear Sir:

The undersigned is empowered to act on behalf of the assignee below (the "Assignee") for the cases listed below. The original Assignment from Honeywell, Inc. to Micron Technology, Inc. is recorded at Reel 012188 and Frame 0697, and each of the listed cases was previously assigned of record to Honeywell, Inc.:

App. No.	Our Ref. No.	Title	Patent No.	Assignment Reel/Frame
09/318,073	MICRON.196CP1	Local Shielding For Memory Cells	PENDING	009993/0887
09/618,237	MICRON.211A	Magneto-Resistive Memory Array	PENDING	011267/0257
09/618,256	MICRON.210A	Magneto-Resistive Memory Having Sense Amplifier With Offset Control	PENDING	011430/0400
09/618,492	MICRON.212A	Memory Redundancy With Programmable Non-Volatile Control	PENDING	011377/0194
09/618,504	MICRON.198A	MRAM Architectures For Increased Write Selectivity	PENDING	012188/0697
09/638,415	MICRON.213A	Magneto-Resistive Memory With Shared Wordline And	PENDING	011189/0647

App. N . : listed below
Filed : listed below

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App. No.	Off. Ref. No.	Sense Line	Patent No.	Assignment Rec'd. Date
09/638,419	MICRON.214A	Passivated Magneto-Resistive Bit Structure And Passivation Method Therefor	PENDING	011287/0078
09/638,637	MICRON.215A	Pulsed Write Techniques For Magneto-Resistive Memories	PENDING	011262/0993
09/668,922	MICRON.216A	Shielding Arrangement To Protect A Circuit From Stray Magnetic Fields	PENDING	011199/0931
08/962,512	MICRON.197A	Shielded Package For Magnetic Devices	5,939,772	9147/0852
08/993,009	MICRON.196A	Self-Aligned Wordline Keeper And Method Of Manufacture Therefor	5,956,267	8940/0969
08/962,518	MICRON.208A	MRAM Design To Reduce Dissimilar Nearest Neighbor Effects	5,982,658	9066/0083
08/940,587	MICRON.207A	Method To Permit High Temperature Assembly Processes For Magnetically Sensitive Devices	6,027,948	N/A
08/993,005	MICRON.209A	Method Of Manufacturing A High Density Magnetic Memory Device	6,048,739	8940/0979
09/365,308	MICRON.206A	Method And Apparatus For Reading A Magneto-Resistive Memory	6,134,138	010147/0942
09/396,189	MICRON.185DV1	Non-Volatile Storage Latch	6,147,922	9115/0907
09/429/664	MICRON.185CP1	Non-Volatile Storage Latch	6,175,525	010353/0253
09/455,850	MICRON.199A	Method And Apparatus For Writing Data States To Non-Volatile Storage Devices	6,178,111	010477/0485
09/059,871	MICRON.185A	Non-Volatile Storage Latch	6,269,027	9115/0907
06/879,679	MICRON.189A	Magneto-Resistive Memory Including Thin Film Storage Cells Having Tapered Ends	4,731,757	4574/0767
06/908,075	MICRON.187A	Differential Arrangement Magnetic Memory Cell	4,751,677	4604/0016
07/008,211	MICRON.195A	Vialess Shorting Bars For Magneto-Resistive Devices	4,754,431	4664/0251
06/870,068	MICRON.188A	Magneto-Resistive Memory With Multi-Layer Storage Cells Having Layer Of Limited	4,780,848	4559/0920

App. No. : listed below
Filed : listed below

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App. No.	Outer Ref. No.	Thickness	Patent No.	Assignment Reel/Frame
07/078,612	MICRON.186A	Differential Magneto-Resistive Memory Sensing	4,829,476	4748/0535
07/168,293	MICRON.192DV1 MICRON.195DV1	Vialess Shorting Bars For Magneto-Resistive Devices	4,897,288	4664/0251
07/161,534	MICRON.194A	Magnetic Device Integrated Circuit Interconnection System	4,918,655	4838/0775
07/404,003	MICRON.203FW1	Semiconductor Device Housing With Magnetic Field Protection	4,953,002	4896/0504
07/504,777	MICRON.191A	Opposed Field Magneto-Resistive Memory Sensing	5,012,444	5269/0357
07/505,090	MICRON.190A	Magnetic State Entry Assurance	5,060,193	5275/0230
07/507,682	MICRON.193A	Inductively Sensed Magnetic Memory	5,064,499	5283/0794
08/061,603	MICRON.202A	Sense Amplifier Input Stage For Single Array Memory	5,349,302	6564/0073
08/365,852	MICRON.204A	Highly Producible Magneto-Resistive RAM Process	5,496,759	7300/0805
08/576,279	MICRON.205A	Integrated Spacer For Magneto-Resistive RAM	5,569,617	7822/0914
08/576,732	MICRON.200A	Magnetic Hardening Of Bit Edges Of Magneto-Resistive RAM	5,756,366	7807/0166

The original assignment from Honeywell, Inc. to Micron Technology, Inc. is recorded at Reel 012188 and Frame 0697. These assignments represents the entire chain of title of these inventions from the Inventors to the Assignee.

I declare that all statements made herein are true, and that all statements made upon information and belief are believed to be true, and further, that these statements were made with the knowledge that willful, false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001, and that willful, false statements may jeopardize the validity of the application, or any patent issuing thereon.

The undersigned hereby revokes any previous powers of attorney in the subject application, and hereby appoints the registrants of Knobbe, Martens, Olson & Bear, LLP, 620 Newport Center Drive, Sixteenth Floor, Newport Beach, California 92660, Telephone (949) 760-0404, Customer No. 20,995, and Michael L. Lynch, Reg. No. 30,871, Micron Technology, Inc., 8000 South Federal Way, P.O. Box 6, Boise, ID 83707-0006, as its attorneys

App. No. : listed below
Filed : listed below

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with full power of substitution and revocation to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected herewith. This appointment is to be to the exclusion of the inventor(s) and his attorney(s) in accordance with the provisions of 37 C.F.R. § 3.71.

Please use Customer No. 20,995 for all communications.

MICRON TECHNOLOGY, INC.

Dated: February 28, 2002

By: Roderic Lewis
Roderic Lewis

Vice President of Legal Affairs,
Title: General Counsel and Corporate Secretary

Address: 8000 South Federal Way
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Boise, ID 83707-0006

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